

B.Sc. (IT) Sem-I (Old)  
**1BIT6 - System Analysis and Design Paper - VI**

P. Pages : 2

Time : Three Hours



**GUG/S/19/1425**

Max. Marks : 80

- 
- Notes :
1. All questions are compulsory and carry equal marks.
  2. Draw neat and labelled diagram and use supporting data wherever necessary.
  3. Avoid vague answers and write specific answer related to question.

**1.** Either:

- a) What is system? Explain various types of system in detail. **8**
- b) Describe the SDLC phases with suitable example. **8**

**OR**

- c) Explain the role of system analyst. Describe the rising position in system Development. **8**
- d) What are considerations for candidate system? What is prototyping? **8**

**2.** Either:

- a) What is the Initial Investigation? Explain the base for planning in System Analysis. **8**
- b) What is feasibility study? Explain the important parameters to be considered for system performance. **8**

**OR**

- c) Explain the tools of structured analysis with pros and cons of each tool. **8**
- d) Describe Information Gathering in detail. Also explain various tools needed for information gathering. **8**

**3.** Either:

- a) What is cost and benefit analysis? Explain the procedure for cost benefit determination. **8**
- b) What is system Design? Explain the process of design. **8**

**OR**

- c) Explain the following terms. **8**
  - i) Input Design
  - ii) Output Design
- d) What is mean by data analysis? Explain in detail. **8**

4. Either:
- a) What is File Structure? Explain the database design for file structure. **8**
  - b) What is Quality Assurance? Describe various levels of Quality Assurance. **8**

**OR**

- c) Distinguish between user documentation and technical documentation. **8**
  - d) What is Test plan? Describe the role of data processing Auditor. **8**
5. Attempt all questions.
- a) Explain the closed system model in detail. **4**
  - b) What is system planning? Explain in detail. **4**
  - c) Explain form design process in short. **4**
  - d) Explain the data structure for file design. **4**

\*\*\*\*\*